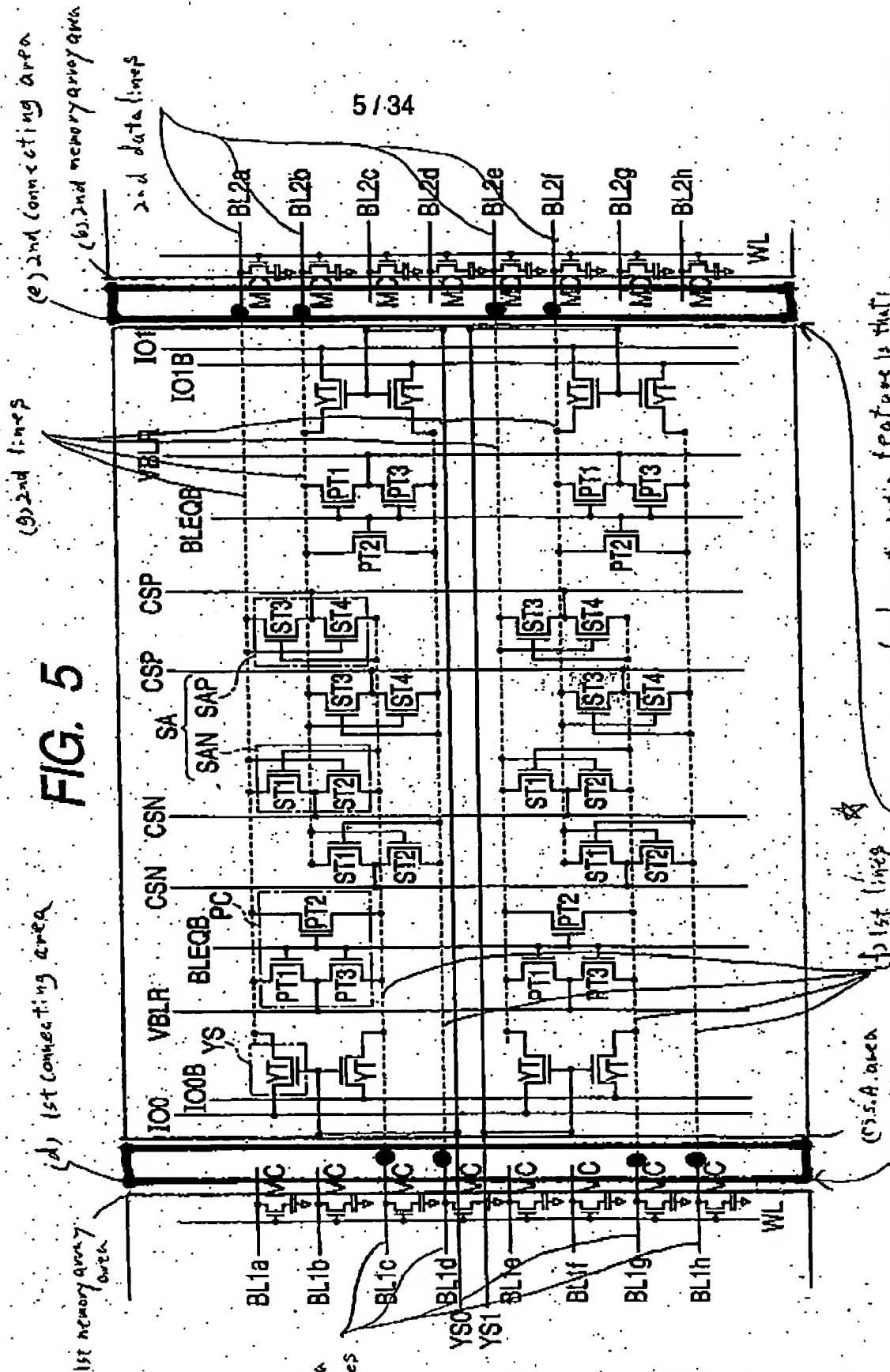




SKETCH 2

FIG. 5



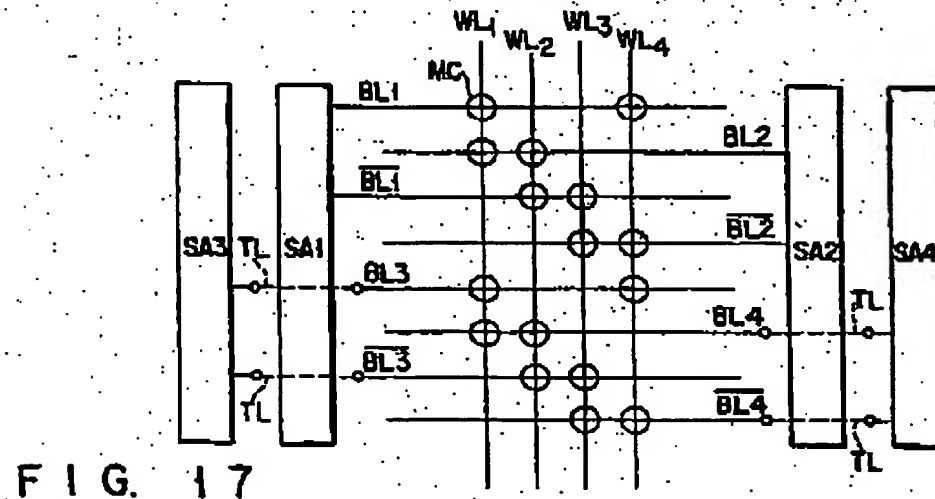
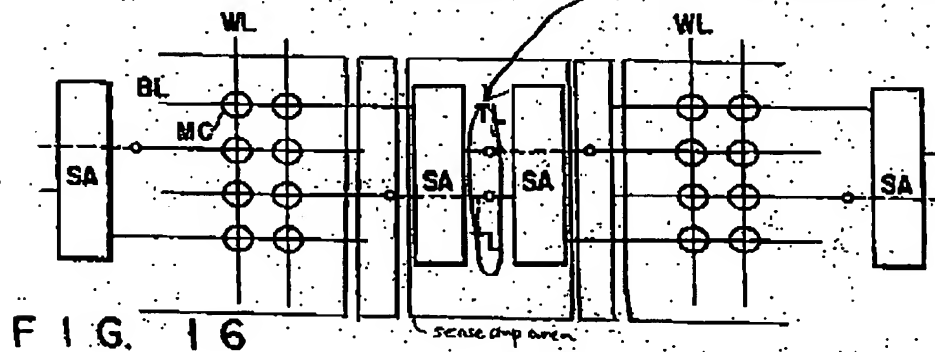
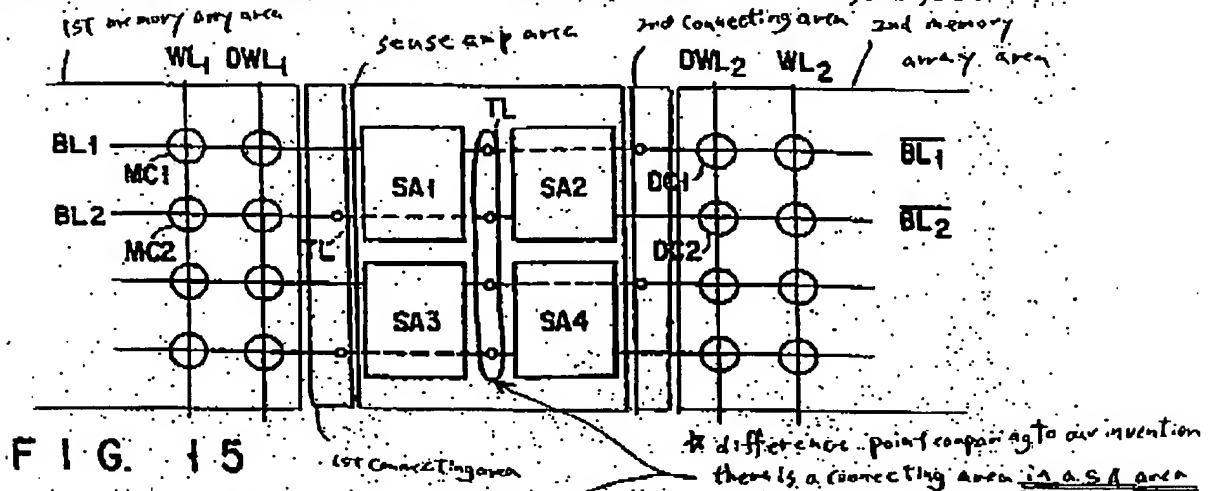
Our invention's characteristic feature is that: each of 1st and 2nd connecting area is formed between 1st (and) 2nd memory array area and sense amp area; and there is no connecting points in a SA area.

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SKETCH 3